



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

TANIGUCHI

Application No. 09/777,897

Filed: February 7, 2001

Art Unit: 2816

Examiner: L. Nguyen

Atty. Docket No. 100353-00039

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF AN OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN AN INPUT SIGNAL AND THE OUTPUT SIGNAL BECOMES AN INTEGRAL NUMBER OF PERIODS OTHER THAN ZERO

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

April 24, 2003

Sir:

In reply to the Office Action dated December 24, 2002, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please amend claim 1 as follows:

D  
1. (Thrice Amended) A delay time adjusting method of adjusting a delay time of an input signal so that a phase of said input signal and a phase of an output signal match each other, based on a comparison between phases of said input signal and said output signal, the method comprising the step of:

increasing the delay time in response to a signal which indicates a start of adjusting the delay time to adjust said phase of said output signal.

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